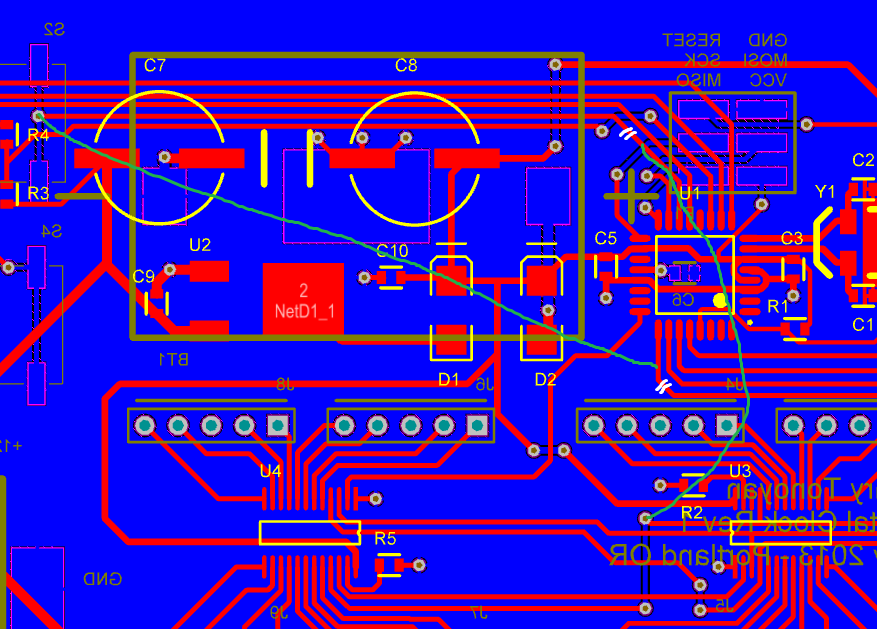
Digital Clock Project Log

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* Modification to PCB: swapping pins 25 & 14 to allow display\_OE (output enable) to be controlled by PWM hardware module.
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